

Kolloquium Hochfrequenztechnik (Vorträge aus Forschung und Industrie)

CMOS Power Amplifiers, Challenges and Trends

von

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Abstract

For the last decades CMOS has been the dominant technology in almost all VLSI design areas. This is because it's low cost advantage and high degree of integration. However, realizing power amplifiers in CMOS technology was always a challenging task especially using the newest nano-Scale CMOS technologies. In this presentation, a short introduction about power amplifiers will be given followed by a discussion about the challenges and possible solutions to enable realizing power amplifiers in Nano-Scale CMOS processes.

Das Institut für Hochfrequenztechnik und Funksysteme lädt alle Interessierten zur Teilnahme an der Veranstaltung ein.

Termin: 30.10.2015, 13:00 Uhr (Dauer der Veranstaltung ca. 30 Min. + Fragen)

Raum: 1712 im Gebäude 3408 (Hochhaus)

Bernd Geck